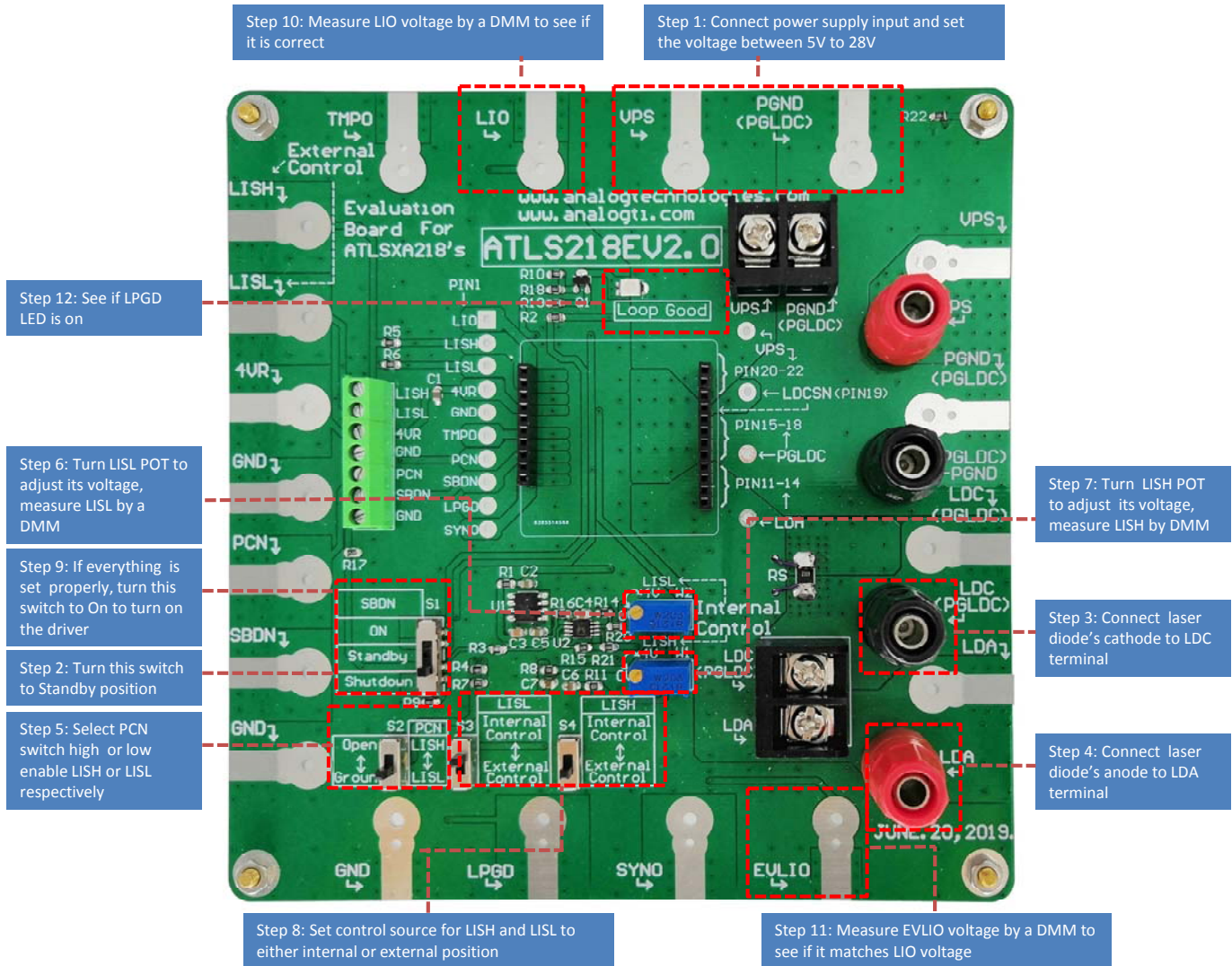




**OPERATION STEPS**

**Figure 3. Operation Steps**

The silkscreen of the evaluation board is shown in Figure 4 with other layers, including top copper, top solder mask, and multilayer (vias and holes).

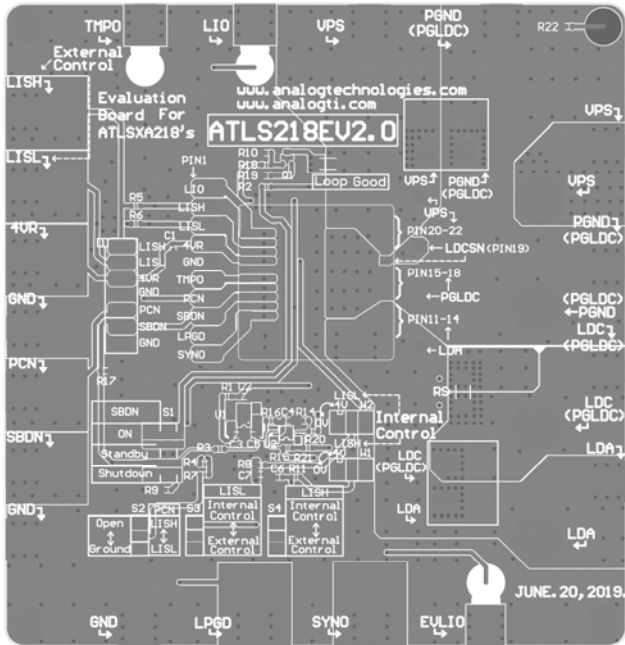


Figure 4. Top Silkscreen with other Top Layers

There is no component on the bottom side of the board, so that is no bottom silkscreen layer image. Figure 6 shows the top layers without the silkscreen. Figure 7 shows the bottom layers, including bottom copper, bottom solder mask, and multilayer.

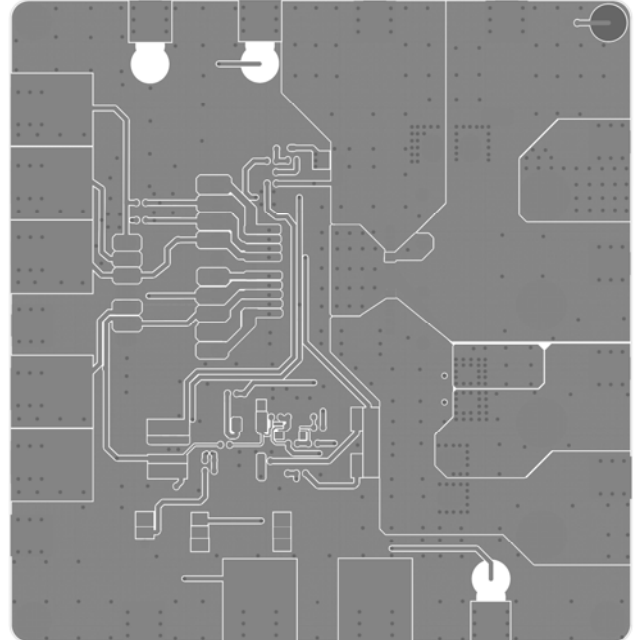


Figure 6. Top Layers without Top Silkscreen

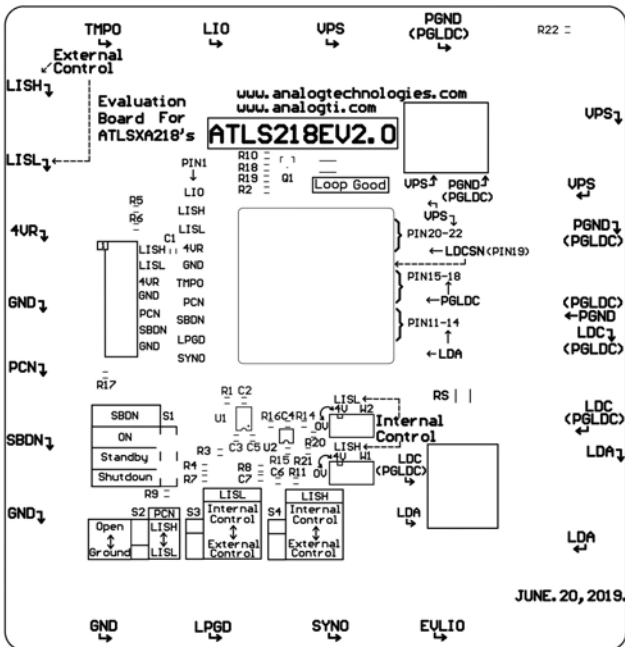


Figure 5. Top Silkscreen only

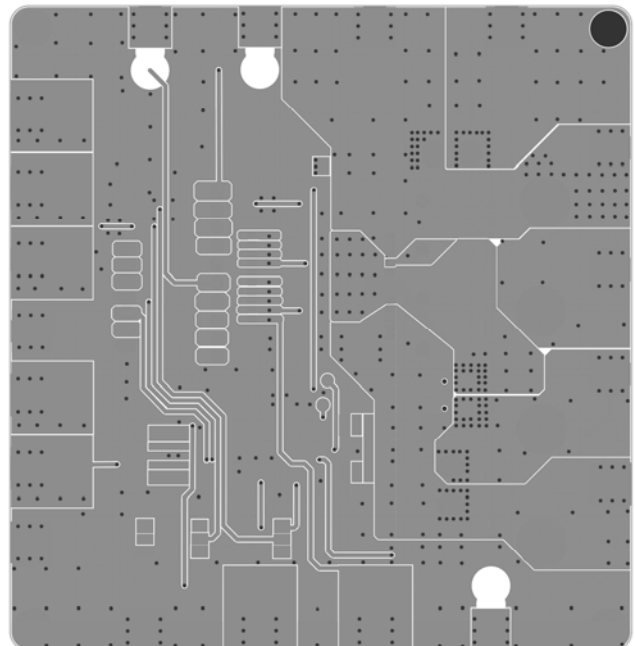


Figure 7. Bottom Layers



The schematic is shown in Figure 8 below.

Please notice that when the laser driver works well, the LPGD pin goes up, which drives the R10 pin 2 high. When the LPGD voltage is high, the voltage is divided by R18 and R10, which turns on the Q1. Therefore, the LED, D1, is on.

The switch S3 and S4 disconnect the POTs on the board when using external sources to modulate the laser driver output currents.

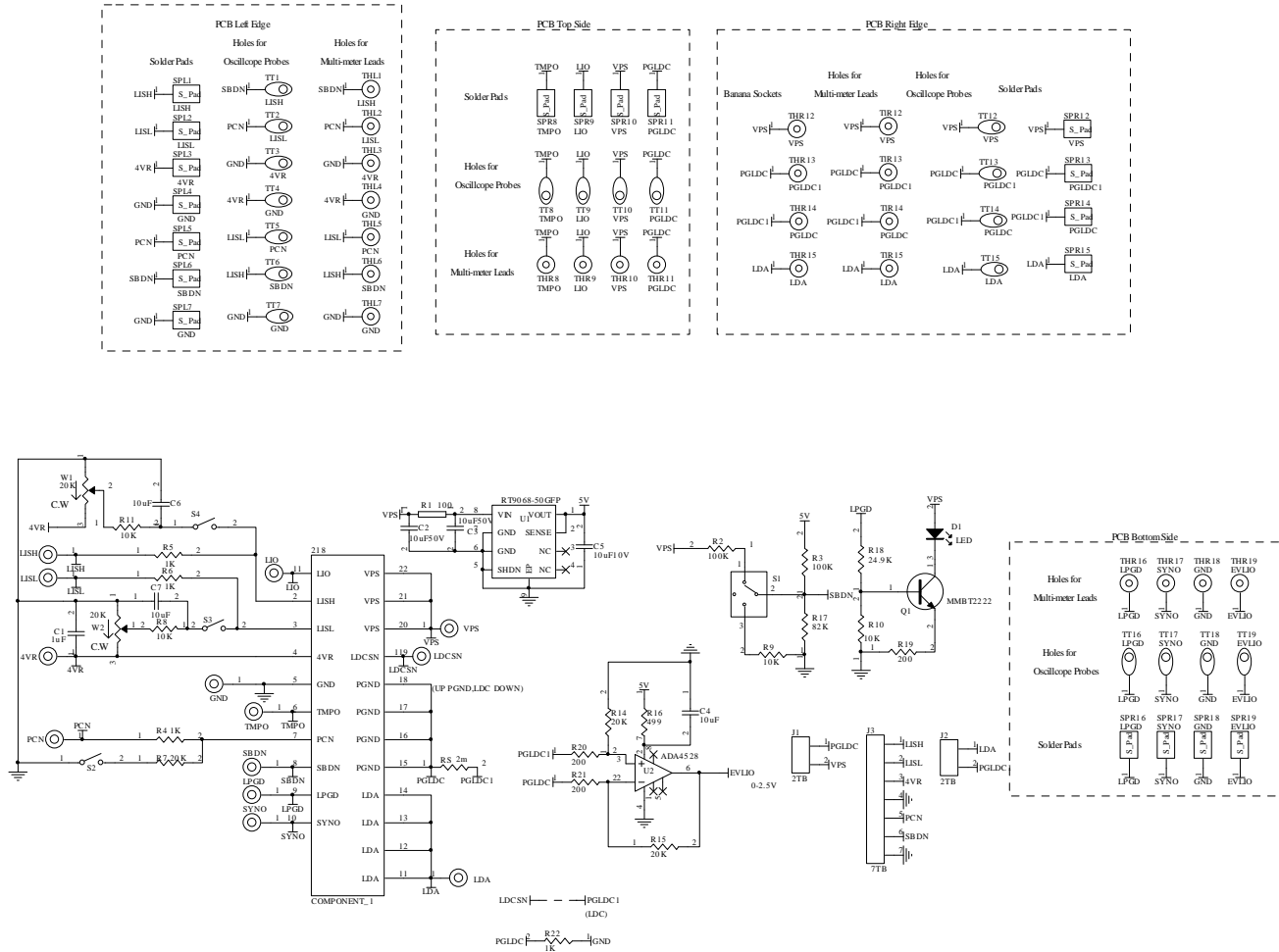


Figure 8. Schematic of Laser Driver Evaluation Board ATLS218EV2.0



**ORDERING INFORMATION**

Part #	Description	1 – 9	≥10
ATLS218EV2.0	Evaluation board for laser drivers ATLSxA218D.	\$88.0	\$82.0

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