

## ATE1-TC-127-7R8AS TEC Module

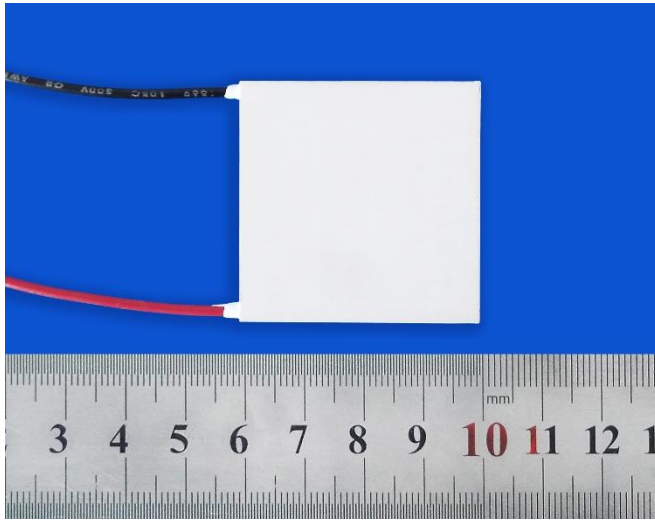


Figure 1. The Photo of Actual ATE1-TC-127-7R8AS

### **THERMAL LIFE STRENGTH:**

1. Maximum temperature for short time (to mount a module into unit): 125°C
2. Recommended maximum operating temperature: 120°C
3.  $DT_{MAX} = 75^{\circ}C@TH=25^{\circ}C$
4.  $\geq 50,000$  cycles by power on for 1 minute and power off for 1 minute
5.  $\geq 20,000$  cycles by driving TEC to 20 °C then 80 °C then 20°C then 80°C...

### **FEATURES**

- Long Life Time than Standard TECs
- Maximum Input Voltage: 16.2V
- 100 % Lead (Pb)-free and RoHS Compliant

### **APPLICATIONS**

Regulate the temperature of the target object with high changing speed and stabilize the temperature to a wide range with high precision. Long life time TEC modules are widely used for temperature cycling applications, including instrumentation, PCR devices, thermal cyclers, chillers and analyzers, etc.

### **DESCRIPTIONS**

This TEC (Thermoelectric Cooler) module, ATE1-TC-127-7R8AS, has 127 pairs of Peltier elements inside with a

maximum voltage of 16.2V. It is designed for temperature cycling applications, in which a TEC module is exposed to demanding physical stresses as the module shifts from heating to cooling, and this can significantly reduce the operational life of a standard TEC. This long life time TECs have significantly longer life time than standard TECs.

This TEC module can be controlled by our TEC controllers to build highly stable and efficient temperature regulating systems. The ATE1-TC-127-7R8AS TEC can be used with our thermistors as well to achieve precise and stable temperature sensing.

The ATE1-TC-127-7R8AS TEC comes in with highly flat bare ceramic surfaces on the both sides, they can be mounted onto flat metal surfaces by inserting thin layers of thermally conductive filler materials, the so-called thermal pads, or placing a thin layer of thermal paste. When mounting, make sure that proper pressure is applied constantly to keep good thermal contacting between the metal and the TEC plates, minimizing thermal resistance between them.

The TECs can withstand strong orthogonal forces applied to the surface, but very vulnerable to tangent forces, especially shocking tangent forces. A small shocking tangent force can cause the Peltier elements crack inside. The crack may not cause operation problem initially, but it will grow with time, causing the TEC resistance to increase slowly, by the end, the TEC will stop operating.

The ATE1-TC-127-7R8AS TEC comes with 2 insulated lead wires. The positive wire is in red color, and the negative wire is of black. The mechanical dimensions are shown in Figure 2, Figure 3 and Table 1.

The TECs, which have the edge area be sealed, are to prevent moisture from getting into the Peltier elements and to extend the life time of the TECs. The advantage of the non-sealed TECs is that the efficiency is higher and can achieve higher maximum temperature difference between the two TEC plates.

For applications in moisture environments, sealed version is recommended, in order to achieve long life time and high reliability for the system.

For high end applications where good and reliable thermal contacts are needed between the TEC and the target object surfaces, the TEC ceramic surface can be metalized so that the TEC and the target object surfaces can be soldered together.

**SPECIFICATIONS**

Table 1. Specifications

Part #	I <sub>MAX</sub> (A)	V <sub>MAX</sub> (V)	Q <sub>MAX</sub> (W)	DT <sub>MAX</sub> * (°C)	Dimension (mm)				Note
					L <sub>c</sub>	L <sub>h</sub>	W	H	
ATE1-TC-127-7R8AS	7.8	16.2	82.1	72.5	40.0	40.0	40.0	3.80	Sealed

\*DT<sub>MAX</sub>: DT stands for Differential Temperature between TEC's 2 plates.

Table 2. Other information

Permitted vertical load	30-60N/cm2 (3-6kgf/cm2)
Recommended storage temperature and humidity	30°C, 60% RH below
Sealing material	Silica gel

**APPLICATION INFORMATION**

As shown in Table 1, the DT<sub>MAX</sub>, the maximum temperature difference between the 2 TEC plates, is 72.5 °C. This is the normal value for a single stage TEC module. When needing a higher DT<sub>MAX</sub>, 2 or 3 stage TECs must be utilized. Contact us for details.

TEC modules can be used for stabilizing laser chip temperature, to stabilize the wavelength and the working lasing mode, resulting in less or no mode hopping and stable output power.

Inversely, when applying a temperature difference between the TEC 2 plates, electricity can be generated. Thus, the TECs can be called TEGs (thermoelectric Generators).

When designing a thermal system by using TECs, one should choose the TEC module in the following way:

1. To achieve the maximum efficiency, it is essential to minimize the thermal resistance between the TEC plate surface and heat sink surface and between the TEC plate and the target object surface. The best way to minimize the thermal resistance is to mount the TEC modules' plates to the heat-sink and to the thermal load by soldering them together. This requires metalizing the TEC plate surfaces first. The 2nd best way is to apply a thin layer of thermal paste between the TEC plates and the heat-sink and the target object surfaces. Constant pressure is needed between the TEC plates and heat-sink and the target object surfaces. Thermal pad material, or so called thermal filler

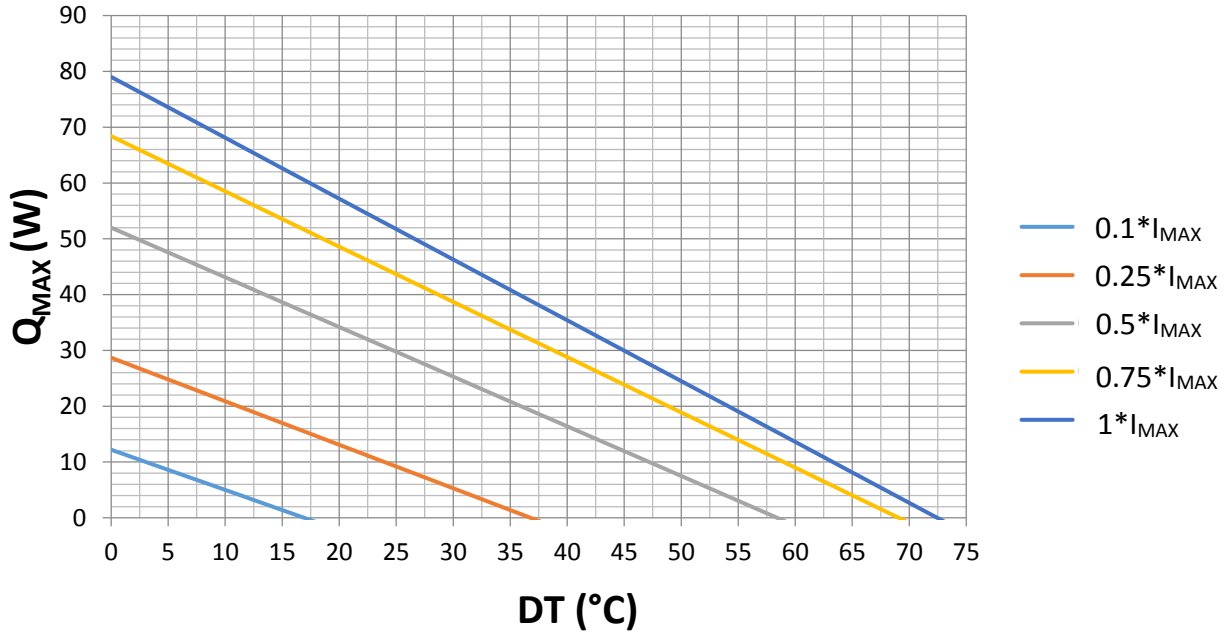
pads, can be used to replace the thermal paste. But this may increase the thermal resistance between the TEC plates and the heat-sink and the target object surfaces. Therefore, thermal paste is recommended to be applied between the TEC plates and the heat-sink. The 3rd way is to use thermally conductive epoxy, to glue the TEC surface and the heat-sink and the target object surfaces together. This approach is the least reliable because the epoxy may lose its adhere power as time goes.

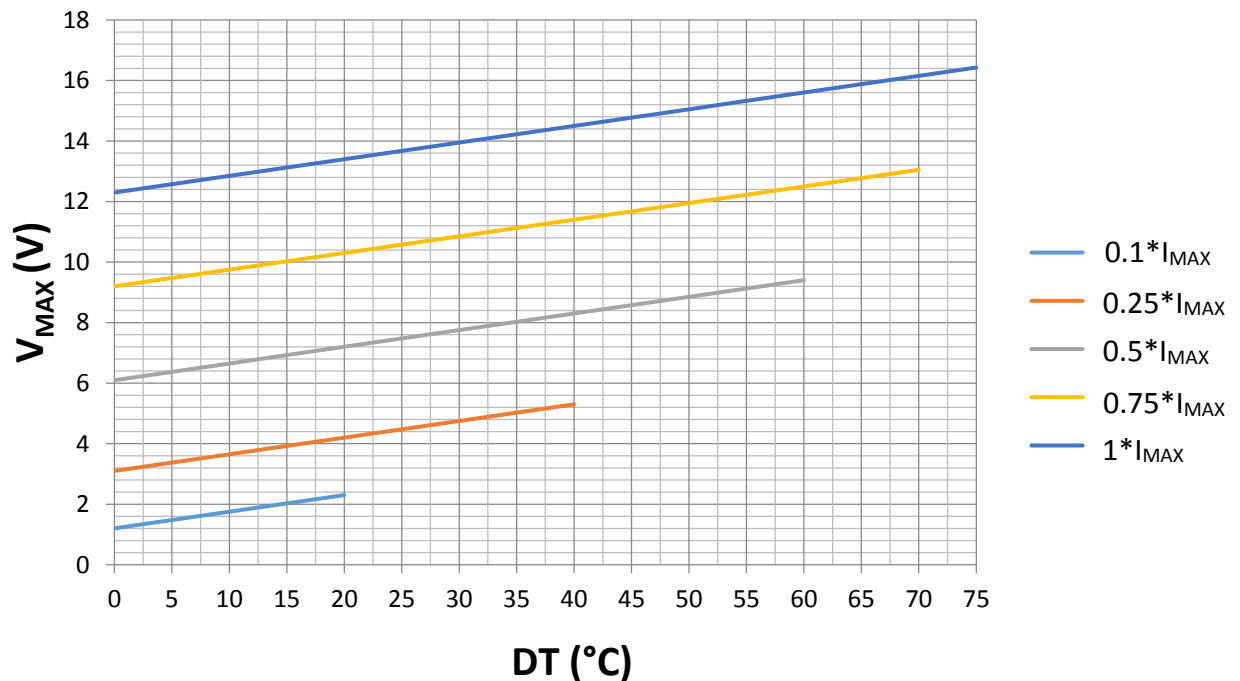
2. To achieve high COP (Coefficient Of Performance), which is defined as:

$$\text{COP} = \text{thermal power} / \text{electric power},$$

the ratio between the TEC's output thermal power and the input electric power. Apparently, a high COP leads to low power system consumption, thus, high efficiency. The key to achieve high COP is to design the system with a low maximum temperature difference between the 2 TEC plates (the hot side and the cold side), DT. When the operating DT can be kept to be  $\leq 30^\circ\text{C}$ , the COP can be as high as 2, a very good result.

3. When the required maximum temperature difference is low, such as  $< 30^\circ\text{C}$ , a large TEC module can be used to drive small thermal load, resulting in a low DT, thus high COP and efficiency.
4. It is not hard to design in a TEC system, but does require some understanding of heat transfer and a good grasp of your applications.

**TYPICAL CHARACTERISTICS**
 **$Q_{MAX}$  vs. DT**

 Figure 2.  $Q_{MAX}$  vs. DT

 **$V_{MAX}$  vs. DT**

 Figure 3.  $V_{MAX}$  vs. DT

**COP vs. DT**

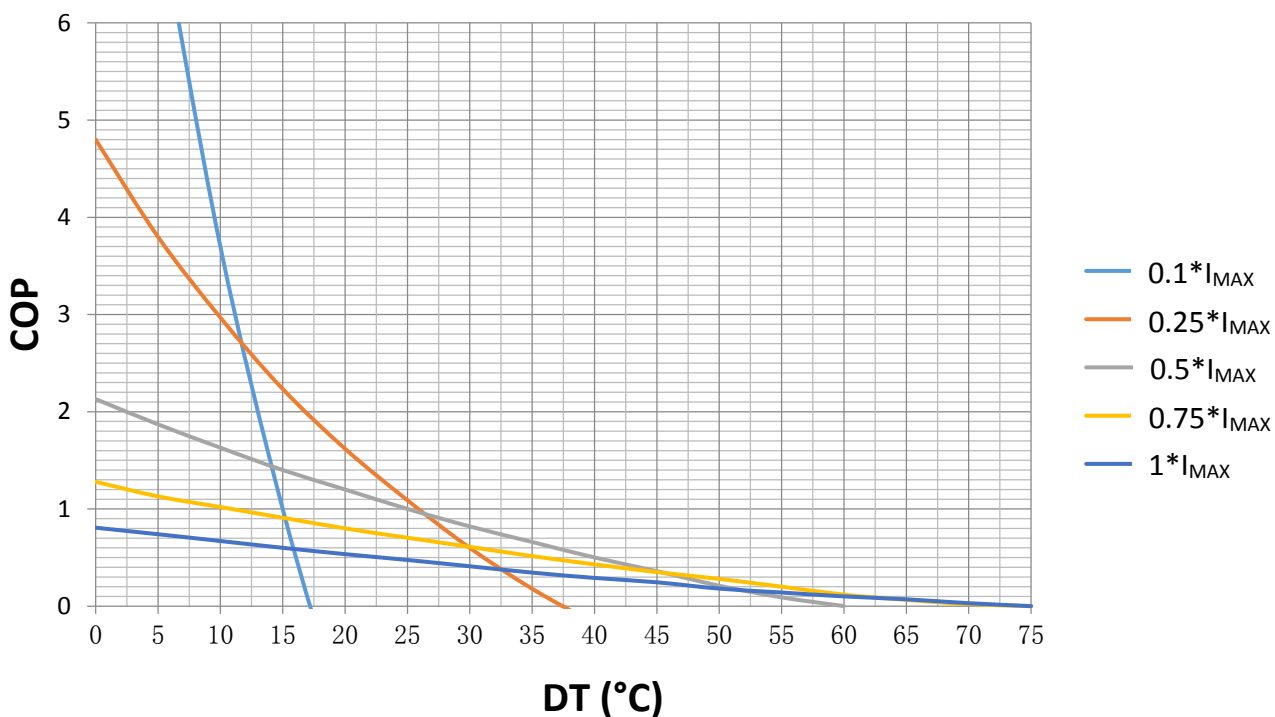


Figure 4. COP vs. DT

**COP vs. I<sub>MAX</sub>**

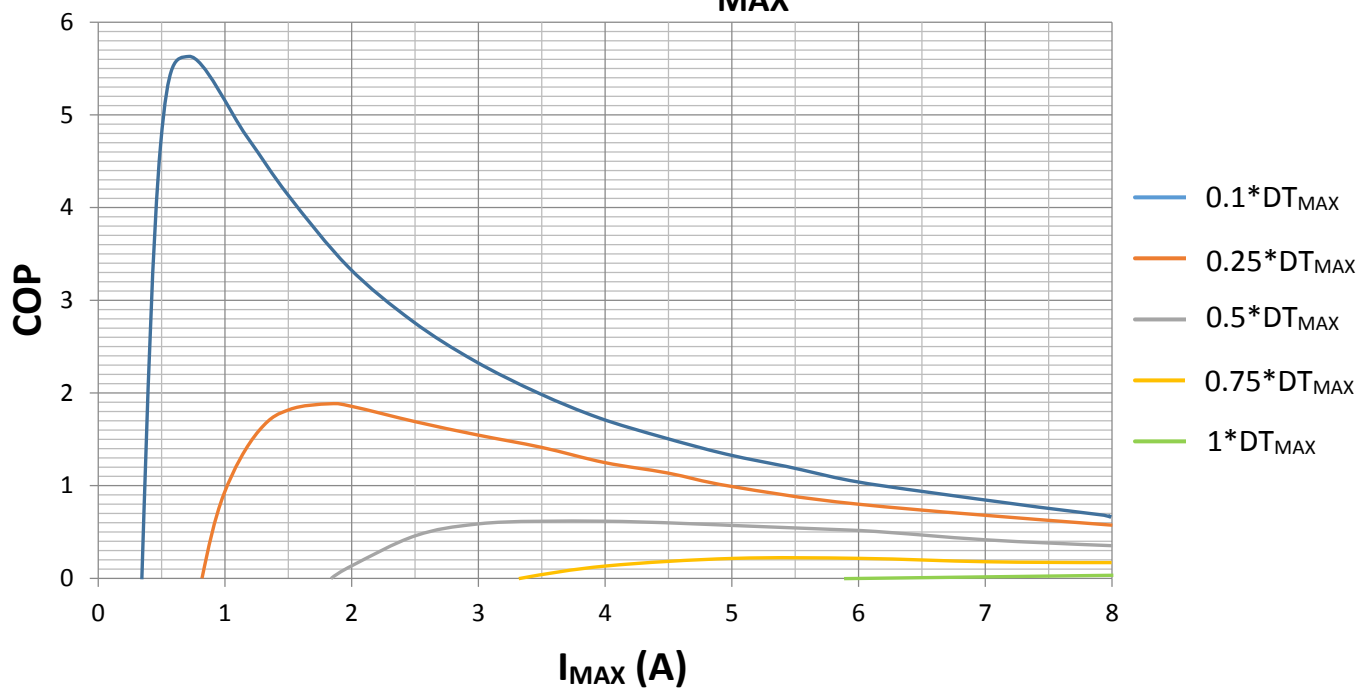


Figure 5. COP vs. I<sub>MAX</sub>

**COMPARASON TEST BETWEEN LONG LIFE TECs AND NORMAL TECs**
**Test Data**

Ambient Temp. ( °C )	Internal Resistance ( $\Omega$ )		No. of Cycles	Temp. of Cycles ( °C )
	LLT*	NOR*		
19	1.3852	1.3778	1	22~55
20	1.3874	1.3862	351	22~75
18	1.379	1.378	1001	
18	1.375	1.376	2001	
19	1.3859	1.3898	2801	10~75
19	1.3867	1.394	3001	
19	1.3708	1.3911	4201	
18	1.3842	1.4046	5001	
16	1.3701	1.3935	6001	
20	1.3819	1.4137	7401	
16	1.3569	1.3981	8201	
18	1.3723	1.4117	9001	
17	1.3658	1.4103	10001	
17	1.3668	1.418	11201	
18	1.3685	1.4352	12801	
21	1.3992	1.4968	14001	
21	1.511	1.548	15201	
17	1.554	1.7908	16801	
21	1.447	1.715	19401	
20	1.427	2.001	20001	

\*LLT: long life TECs; NOR: normal TECs.

**Internal Resistance (I. R.) Change**

Parameter	LLT ( $\Omega$ )	NOR ( $\Omega$ )	No. of Cycles
I. R. Change	0.0418 ( $\Omega$ )	0.6232 ( $\Omega$ )	20000
Change Rate	3%	45%	

**MECHANICAL DIMENSIONS**

The mechanical dimensions of the ATE1-TC-127-7R8AS is shown below.

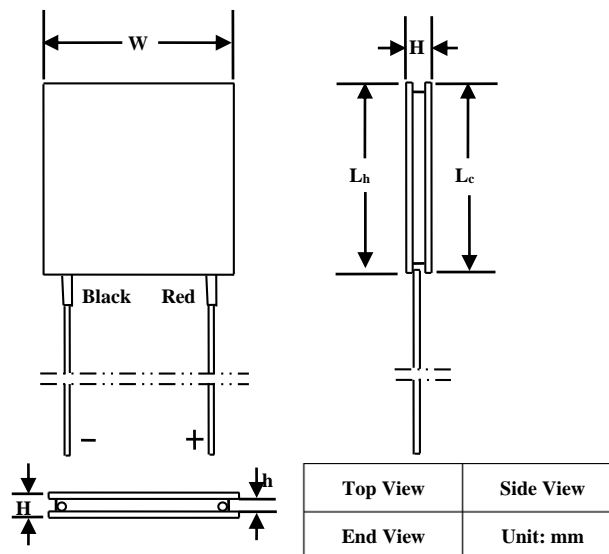


Figure 6. Mechanical Dimensions of Sealed ATE1-TC-127-7R8AS

**Note:** As Figure 6 shows, when the red lead wire is on the right, then the top surface is the cold side of the TEC.

**CAUTIONS**

1. Never apply electricity to TEC modules without having heat sinks attached properly.
2. Always keep the current less than  $I_{MAX}$ , to avoid thermal run-away disaster.

**NOTICE**

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