ATE1-17-1.4A TEC Module

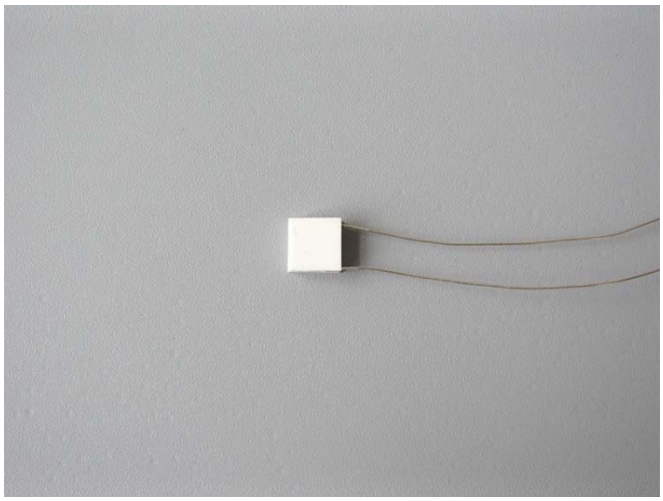

Figure 1. The Photo of Actual ATE1-17-1.4A

FEATURES

- Maximum Input Voltage: 2.2 V
- Low Cost
- Long Life Time
- 100 % Lead (Pb)-free and RoHS Compliant

APPLICATIONS

Regulate the temperature of the target object with high changing speed and stabilize the temperature to a wide range with high precision. Widely used in solid state lasers, optical components, CCD's, IR cameras, bio-tech testing benches, etc.

DESCRIPTIONS

This TEC (Thermoelectric Cooler) module, ATE1-17-1.4A, has 17 pairs of Peltier elements inside with a maximum voltage of 2.2 Volt. They are designed for regulating the temperature of the target objects precisely and can be controlled by our TEC controllers to build highly stable and efficient temperature regulating systems. The ATE1-17-1.4A TEC can be used with our thermistors as well to achieve precise and stable temperature sensing.

The ATE1-17-1.4A TEC comes in with highly flat bare ceramic surfaces on the both sides, they can be mounted onto flat metal surfaces by inserting thin layers of thermally conductive filler materials, the so-called thermal pads, or placing a thin layer of thermal paste. When mounting, make sure that proper pressure is applied constantly to keep good thermal contacting between the metal and the TEC plates, minimizing thermal resistance between them.

The TECs can withstand strong orthogonal forces applied to the surface, but very vulnerable to tangent forces, especially shocking tangent forces. A small shocking tangent force can cause the Peltier elements crack inside. The crack may not cause operation problem initially, but it will grow with time, causing the TEC resistance to increase slowly, by the end, the TEC will stop operating.

In the part number, ATE1-17-1.4A, "1.4A" indicate the maximum current allowed for entering the TEC module. It can achieve a maximum temperature difference, DT_{MAX}, of 72.5°C.

The ATE1-17-1.4A TECs come with 2 insulated lead wires. The positive wire is in red color with the length of 13 inches, 340mm, and the negative wire is of black, with the same length. The mechanical dimensions are shown in Figure 6.

The TECs with sealed edge can prevent moisture from getting into the Peltier elements and to extend the life time of the TECs. The advantage of the non-sealed TECs is that the efficiency is higher and can achieve higher maximum temperature difference between the two TEC plates.

For applications in moisture environments, sealed version is recommended, in order to achieve long life time and high reliability for the system.

For high end applications where good and reliable thermal contacts are needed between the TEC and the target object surfaces, the TEC ceramic surface can be metalized so that the TEC and the target object surfaces can be soldered together.

1161 Ringwood Ct, #110, San Jose, CA 95131, U. S. A. Tel.: (408) 748-9100, Fax: (408) 770-9187 www.analogtechnologies.com
©Copyrights 2000-2022, Analog Technologies, Inc. All Rights Reserved. Updated on 12/6/2022 Email: staff@analogti.com/sales@analogti.com

SPECIFICATIONS

Table 1

Part #	I_{MAX} (A)	V_{MAX} (V)	Q_{MAX} (W)	DT_{MAX} (°C)	Dimension (mm)				1–4 PCs	5–24 PCs	25–99 PCs	≥100 PCs	Note
					L_c	L_h	W	H					
ATE1-17-1.4A	1.4	2.2	2	72.5	6		6	2.47	$37.68	$36.25	$35.64	$34.07	Non- sealed

* DT_{MAX}: DT stands for Differential Temperature between TEC's 2 plates

APPLICATION INFORMATION

As shown in Table 1, the DT_{MAX}, the maximum temperature difference between the 2 TEC plates, is 72.5°C. This is the normal value for a single stage TEC module. When needing a higher DT_{MAX}, 2 or 3 stage TECs must be utilized. Contact us for details.

TEC modules can be used for stabilizing laser chip temperature, to stabilize the wavelength and the working lasing mode, resulting in less or no mode hopping and stable output power.

Inversely, when applying a temperature difference between the TEC 2 plates, electricity can be generated. Thus, the TECs can be called TEGs (thermoelectric Generators).

When designing a thermal system by using TECs, one should choose the TEC module in the following way:

1. To achieve the maximum efficiency, it is essential to minimize the thermal resistance between the TEC plate surface and heat sink surface and between the TEC plate and the target object surface. The best way to minimize the thermal resistance is to mount the TEC modules' plates to the heat-sink and to the thermal load by soldering them together. This requires metalizing the TEC plate surfaces first. The 2nd best way is to apply a thin layer of thermal paste between the TEC plates and the heat-sink and the target object surfaces. Constant pressure is needed between the TEC plates and heat-sink and the target object surfaces. Thermal pad material, or so called thermal filler pads, can be used to replace the thermal paste. But this may increase the thermal resistance between the TEC plates and the heat-sink and the target object surfaces. Therefore, thermal paste is recommended to be applied between the TEC plates and the heat-sink. The 3rd best way is to use thermally conductive epoxy, to glue the TEC surface and the heat-sink and the target object surfaces together. This approach is the least reliable because the epoxy may lose its adhere power as time goes.

2. To achieve high COP, Coefficient of Performance, which is defined as:

   COP = thermal power / electric power,

   the ratio between the TEC's output thermal power and the input electric power. Apparently, a high COP leads to low power system consumption, thus, high efficiency. The key to achieve high COP is to design the system with a low maximum temperature difference between the 2 TEC plates (the hot side and the cold side), DT. When the operating DT can be kept to be ≤ 30°C, the COP can be as high as 2, a very good result.

3. When the required maximum temperature difference is low, such as < 30°C, a large TEC module can be used to drive small thermal load, resulting in a low DT, thus high COP and efficiency.

4. It is not hard to design in a TEC system, but does require some understanding of heat transfer and a good grasp of your applications.

©Copyrights 2000-2022, Analog Technologies, Inc. All Rights Reserved. Updated on 12/6/2022

TYPICAL CHARACTERISTICS

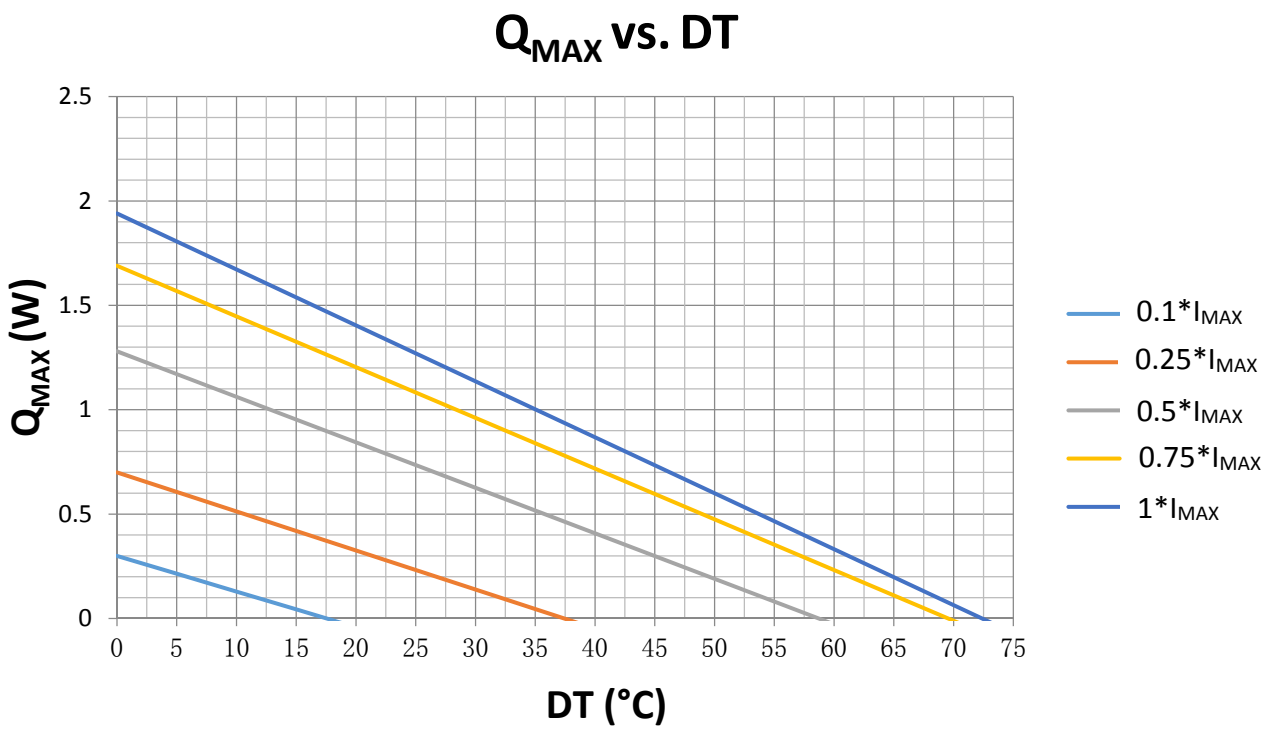

Figure 2. Q_{MAX} vs. DT

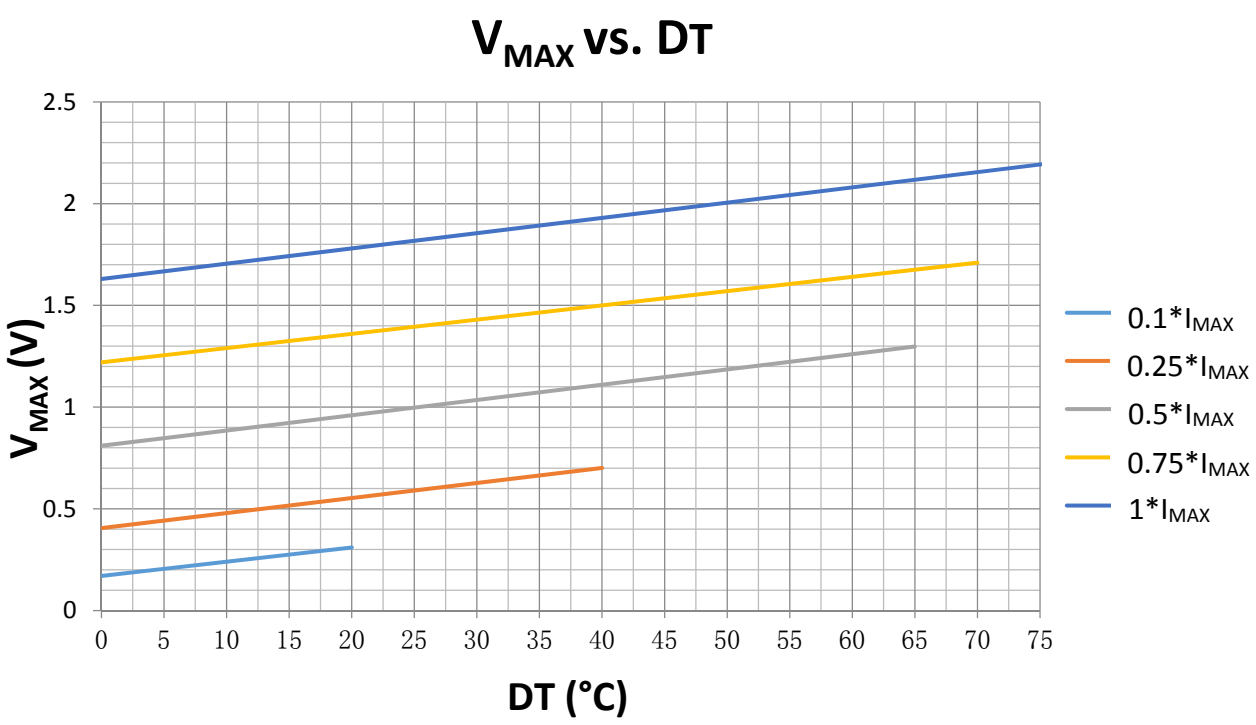

Figure 3. V_{MAX} vs. DT

©Copyrights 2000-2022, Analog Technologies, Inc. All Rights Reserved. Updated on 12/6/2022

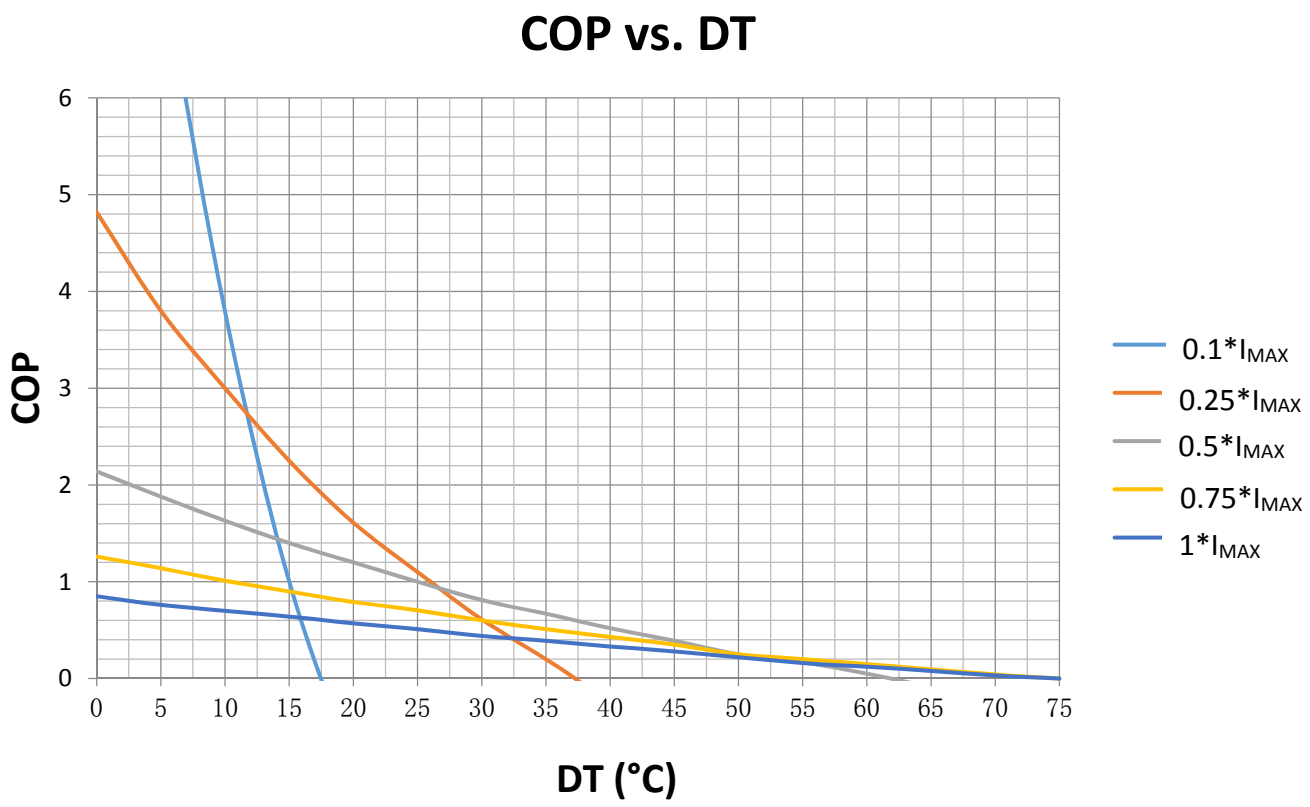

Figure 4. COP vs. DT

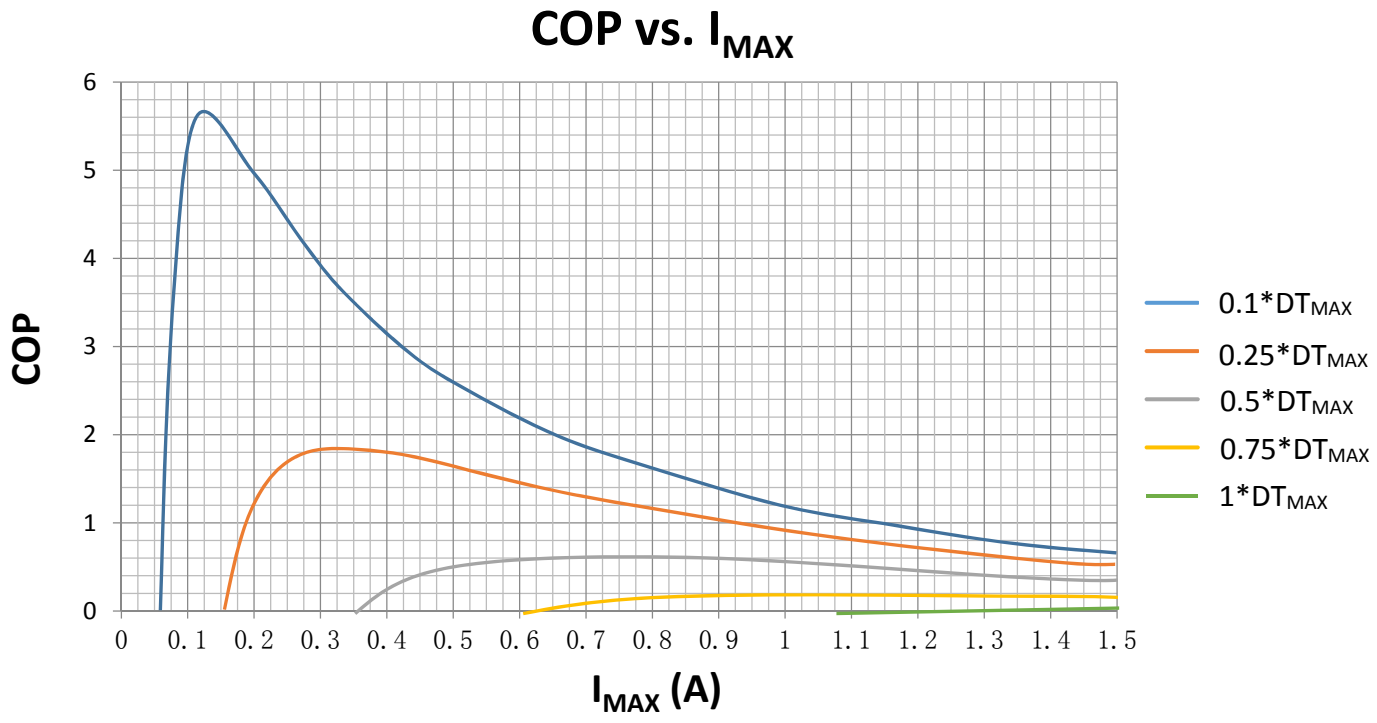

Figure 5. COP vs. I_{MAX}

©Copyrights 2000-2022, Analog Technologies, Inc. All Rights Reserved. Updated on 12/6/2022

MECHANICAL DIMENSIONS

The mechanical dimensions of the ATE1-17-1.4A are shown below.

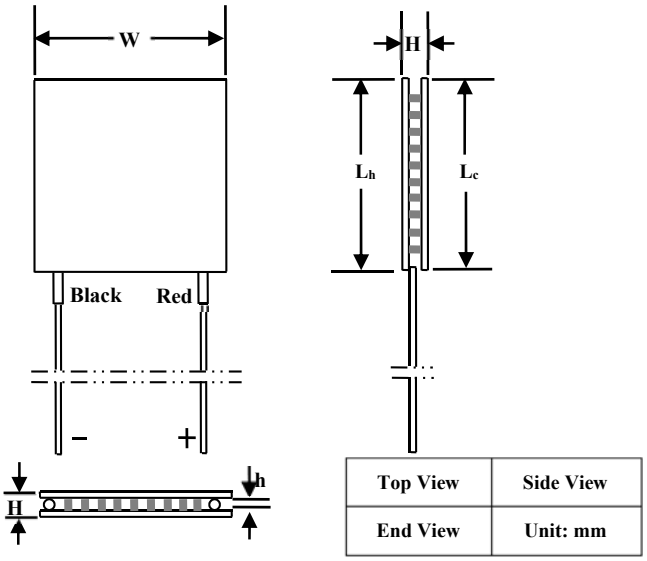

Figure 6. Mechanical Dimensions of ATE1-17-1.4A

Note: As Figure 6 shows, when the red lead wire is on the right, then the top surface is the cold side of the TEC.

CAUTIONS

1. Never apply electricity to TEC modules without having heat sinks attached properly.
2. Always keep the current less than I_{MAX}, to avoid thermal run-away disaster.

NOTICE

1. ATI reserves the right to make changes to its products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete.
2. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability. Testing and other quality control techniques are utilized to the extent ATI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.
3. Customers are responsible for their applications using ATI components. In order to minimize risks associated with the customers' applications, adequate design and operating safeguards must be provided by the customers to minimize inherent or procedural hazards. ATI assumes no liability for applications assistance or customer product design.
4. ATI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of ATI covering or relating to any combination, machine, or process in which such products or services might be or are used. ATI's publication of information regarding any third party's products or services does not constitute ATI's approval, warranty or endorsement thereof.
5. IP (Intellectual Property) Ownership: ATI retains the ownership of full rights for special technologies and/or techniques embedded in its products, the designs for mechanics, optics, plus all modifications, improvements, and inventions made by ATI for its products and/or projects.

©Copyrights 2000-2022, Analog Technologies, Inc. All Rights Reserved. Updated on 12/6/2022