

Figure 1.1. Top View of AHV24V40KV1MAW



Figure 1.2. Side View



Figure 1.4. Side View



Figure 1.3. Bottom View



Figure 1.5. Side View

AHV24V40KV1MAW

FEATURES

• Input Power Voltage: 24V ± 1V

• Input Current Range: 550mA to 2.2A

Output Voltage: 0 to 40kV@CTRL = 0 to 5V

Monitor Voltage: 0 to 4V
Max. Output Current: 1mA
Reference Voltage: 5V ± 0.05V
Input Control Voltage: 0 to 5V

• Full Span Modulation on Output Voltage

Electronic Shutdown Control

APPLICATIONS

This power module, AHV24V40KV1MAW, is designed for achieving DC-DC conversion from low voltage to high voltage as a power supply source which is widely used

in scientific research and other fields including:

- X-ray Machine
- Spectral Analysis
- Nondestructive Inspection
- Semiconductor Manufacturing Equipment
- CRT Monitor Test
- Particle Accelerator
- Capillary Electrophoresis
- Nondestructive Detection
- Particles Injection
- Semiconductor Technology
- Physical Vapor Phase Deposition
- Radio Frequency Amplification
- Electrospinning Preparation of Nanofiber
- Glass / Fabric Coating
- DC Reactive Magnetron Sputtering

DESCRIPTION

Figure 2 shows the connecting wires of AHV24V40KV1MAW, of which their detail information given in Table 1. The output voltage can be set to a constant value by connecting the CTRL port to the central tap of a POT (Potentiometer) corresponding to 0V to 40kV proportionally at the output VOUT port as shown in Figure 3.



Figure 2. The Connecting Lead Wires of AHV24V40KV1MAW

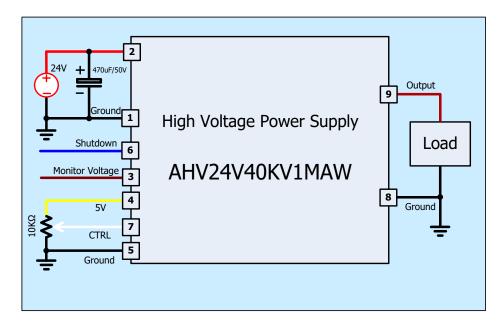


Figure 3. Setting Output to be a Constant Voltage

Table 1. Pin Names, Colors, Functions and Specifications.

| No. | Name | Co | olor | Туре | Description | Min. | Тур. | Max. |
|-----|------|--------|------|---|----------------------------|------|------|------|
| 1 | GND | Black | • | Ground for analog, digital and power signals. | Input GND | | 0V | |
| 2 | VPS | Red | | Power input | Input voltage | | 24V | |
| 3 | MON | Brown | | Analog output | Monitor Voltage | 0V | | 4V |
| 4 | 5VR | Yellow | | Analog output | Reference voltage | | 5V | |
| 5 | GND | Black | • | Ground for analog, digital and power signals. | Control GND Monitor GND | | 0V | |
| 6 | CDN | Dlug | | Digital input | Shutdown logic low | 0V | | 0.8V |
| 0 | SDN | Blue | | Digital input | Shutdown logic high | 1.2V | | 5V |
| 7 | CTRL | White | | Analog input | Regulation | 0V | | 5V |
| 8 | GND | Black | | Power output | Output GND | | 0V | |
| 9 | VOUT | Brown | | Power output | Output high voltage | 0V | | 40kV |



Please note that the modulation signal must have a low frequency \leq 10Hz and the value range must be $0V \leq V_{CTRL} \leq 5V$. The equivalent input circuit for the MON port is shown in Figure 4.

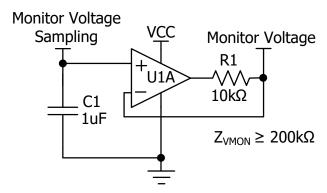


Figure 4. The Equivalent Circuit for MON Port The equivalent input circuit for the CTRL is shown in Figure 5.

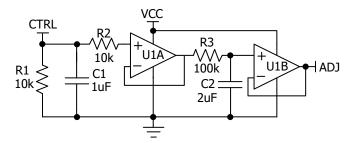


Figure 5. The Equivalent Circuit for CTRL Port

To shutdown AHV24V40KV1MAW, pull down SDN pin to <0.8V; to turn it on, leave SDN pin unconnected or pull it >1.2V. The maximum voltage allowed on the SDN pin is 5V. The equivalent circuit for SDN port is shown in Figure 6.

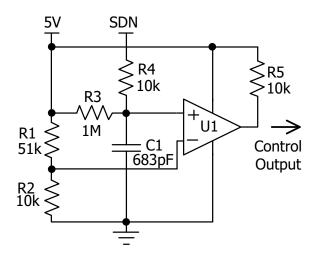


Figure 6. The Equivalent Circuit for SDN Port

USING AHV24V40KV1MAW

This high voltage power supply must be mounted tightly onto a metal plate, ideally, thus expanding its heating sinking capacity of the metal enclosure. Sufficient ventilation must be provided to keep the power supply surface temperature under 55°C.

SAFETY PRECAUTIONS

Although AHV24V40KV1MAW high voltage power supply comes with an over current protection circuit, a short circuit at the output should always be avoided. Make sure the high voltage wire for connecting VOUT node has sufficient insulation capability with its surrounding objects.



SPECIFICATIONS

Table 2. Characteristics. $T_A = 25$ °C, unless otherwise noted.

| Parameter | | Symbol | Test Conditions | Min. | Тур. | Max. | Unit/Note |
|---|---|--|---|-----------------------|-------|------------------------|------------------|
| Input Power Voltage | | V _{VPS} | | 23 | 24 | 25 | V |
| Input Power C | Input Power Quiescent Current | | I _{VOUT} = 0mA | 450 | 500 | 550 | mA |
| Input Power Co | urrent at Full Load | I _{VPS_FL} | I _{VOUT} = 1mA | 2.1 | 2.2 | 2.3 | Α |
| • | ver Current at utdown | Ivps_shdn | $T_A = -10^{\circ}C \sim 55^{\circ}C$ | | 15 | | mA |
| | Modulation Voltage Range Frequency on CTRL | | | 0 | | 12 | Hz |
| Chutdown | Dort Current | I _{SDNL} | V _{SDNL} < 0.8V | -5 | | -4.2 | μΑ |
| Shutdown | Port Current | I _{SDNH} | 1.2V < V _{SDNL} < 5V | 0 | | 3.8 | μΑ |
| Shutdown Vo | oltage Logic Low | V _{SDNL} | | 0 | | 0.8 | V |
| Shutdown Vo | ltage Logic High | V_{SDNH} | | 1.2 | | 5 | V |
| Outpo | ut Voltage | V _{VOUT} | $I_{VOUT} = 0 \sim 1 mA$ | 0 | | 40000 | V |
| Output Co | Output Current Range | | V _{VPS} = 23V ~ 25V | 0 | | 1 | mA |
| Reference Voltage Output Range | | V _{5VR} | $T_{\text{A}} = -10^{\circ}\text{C} \sim 55^{\circ}\text{C}$ $I_{\text{5VR}} \leq 5\text{mA}$ | 4.95 | 5 | 5.05 | V |
| Monitor Voltag | Monitor Voltage Out Impedance | | | | 1 | | ΜΩ |
| Monito | Monitor Voltage | | $V_{OUT} = 0 \sim 40 \text{kV}$ | 0 | | 4 | V |
| Output I | Load Range | | | 40 | | ∞ | ΜΩ |
| Output Vo | Output Voltage Ripple | | Bandwidth = 1MHz $R_{\text{LOAD}} = 40 \text{ M}\Omega$ | | ≤40 | | V _{P-P} |
| Output Voltage Temperature Coefficient | | TCV _{VOUT} (2) | $\begin{aligned} V_{VPS} &= 24V \\ V_{CTRL} &= V_{5VR} = 5V \\ V_{VOUT} &= 40kV \\ I_{VOUT} &= 1mA \\ T_A &= -10^{\circ}C \sim 55^{\circ}C \end{aligned}$ | | ≤0.1 | | %/°C |
| Output Voltage Range v.s. Temperature | | Vvouт(T) | $V_{VPS} = 24V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = 40kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$ | 0.99V _{уоит} | Vvout | 1.01V _V OUT | V |
| Output | Short Term Drift | $\frac{\left \Delta V_{VOUT}/V_{VOUT}\right }{\Delta t \text{ (min)}}$ | $V_{VPS} = 24V$ $V_{CTRL} = V_{5VR} = 5V$ | | ≤0.3 | | %/min |
| Voltage Drift | Long Term Drift | $\frac{\left \Delta V_{VOUT}/V_{VOUT}\right }{\Delta t (h)}$ | $V_{VOUT} = 40kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$ | | ≤0.5 | | %/h |

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AHV24V40KV1MAW

| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit/Note |
|---|--|--|----------------|-------|------|-----------|
| Output Voltage Rise Time | t _r | $V_{VOUT}(t_1) = 4kV$ $V_{VOUT}(t_2) = 36kV$ No-Load | | 30 | | ms |
| Output Voltage Fall Time | t _f | $V_{VOUT}(t_2) = 36kV$ $V_{VOUT}(t_3) = 4kV$ No-Load | | 100 | | ms |
| Mean Time Between Failure | MTBF | | | 1M | | h |
| Instantaneous Short Circuit Current at the Output | I _{VOUT_SC} | | | ≤150 | | mA |
| Load Regulation | $\frac{\left \Delta V_{\text{VOUT}}/V_{\text{VOUT}}\right }{\Delta I_{\text{VOUT}}}$ | $V_{VOUT} = 40kV$ $I_{VOUT} = 1mA$ | | ≤0.05 | | %/mA |
| Full Load Efficiency | η ⁽³⁾ | $V_{VPS} = 24V$ $V_{VOUT} = 40kV$ $I_{VOUT} = 1mA$ | | ≥75 | | % |
| Operating Temperature Range | T _{opr} | | -10 | | 55 | °C |
| Storage Temperature Range | T _{stg} | | -20 | | 85 | °C |
| Francis Dinamaiana | | | 170×100×55 | | mm | |
| External Dimensions | | | 5.51×6.69×2.17 | | inch | |
| | | | | 1200 | | g |
| Weight | | | | 2.65 | | lbs |
| | | | | 42.33 | | Oz |



TESTING DATA

Test conditions: $V_{VPS} = 24V$, $T_A = 25^{\circ}C$, $R_{LOAD} = 40M\Omega$

DC Testing

The measured output voltage, V_{VOUT}, corresponding to the control port input voltage, V_{CTRL}, is shown in Figure 7.

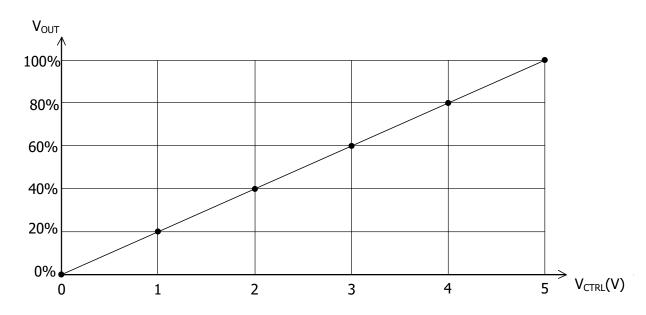


Figure 7. V_{CTRL} vs. V_{VOUT}

AC Testing

To test the analog modulation function, a triangle and sine-wave voltage signals of $0.25V \sim 5V$, f = 0.10Hz, are applied to the CTRL port as the input source signal respectively. Figure 8 and 9 show both the input signal and the output signal waveforms when using the triangle and sine-wave signals at the CTRL port respectively.

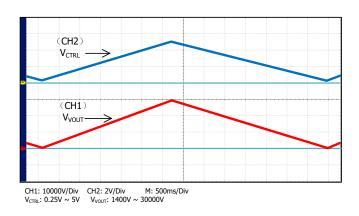


Figure 8. Input vs. Output Waveforms for Triangle Wave Control

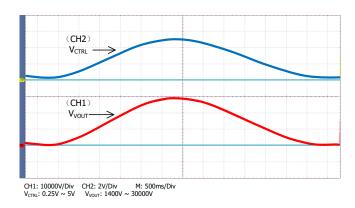


Figure 9. Input vs. Output Waveforms for Sine Wave Control

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To test the rise and fall times at the output, a step function signal is applied to the CTRL port. The testing results are shown in Figure 10, Figure 11, and Figure 12. As shown in Figure 11 and Figure 12, a square wave of $0.25V \sim 5V$, f = 0.10Hz, is applied to CTRL port, the output waveform fall time is measured to be about 100ms and the rise time is about 30ms. These two values are not the same, that is because on the rising trail, the power supply injects a current to the load; while on the falling trail, the best the power supply can do is to stop its output current and let the load resistor drain the output filtering capacitor to a lower voltage, and the draining current is much smaller than the injection current.

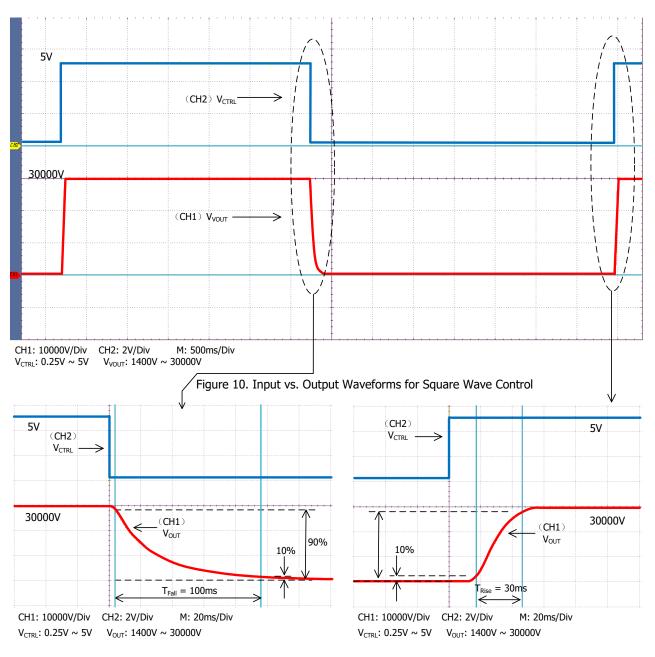
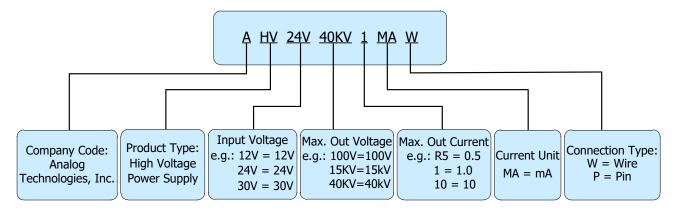


Figure 11. Falling Trail for Large Signal Response

Figure 12. Rising Trail for Large Signal Response

NAMING PRINCIPLE



Naming Principle of AHV24V40KV1MAW

DIMENSIONS

Connecting Lead Wire Sizes and Lengths

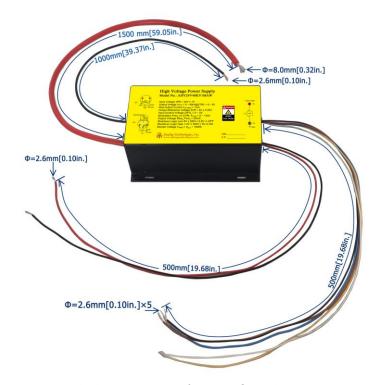


Figure 13. Connecting Lead Wires of AHV24V40KV1MAW

| Lead Wires | | neter | Length | |
|---|-----|-------|----------|------------------|
| | | inch | mm | inch |
| Thick brown lead wire | 8.0 | 0.32 | 1500 ± 1 | 59.05 ± 0.039 |
| Black lead wire | 2.6 | 0.10 | 1000 ± 1 | 39.37 ± 0.039 |
| Yellow, red, blue, black and white lead wires | 2.6 | 0.10 | 500 ± 1 | 9.68 ± 0.039 |



Outline Dimensions

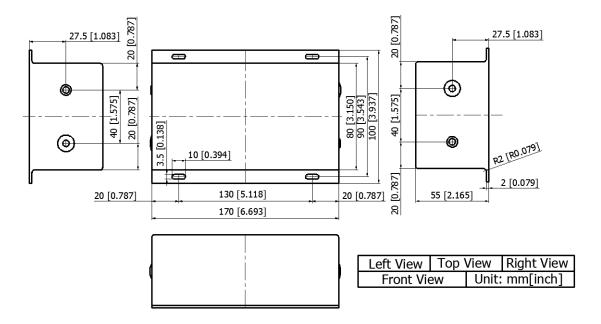


Figure 14. Outline Dimensions

ORDERING INFORMATION

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High Voltage Power Supply



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