

**FEATURES**

- High Efficiency:  $\geq 90\%$
- Constant Current Output
- Maximum Output Current: 2A
- Current Output Noise: 0.05%
- High Stability: 100ppm/°C
- Zero EMI
- Compact Size
- DIP Package Available
- 100 % Lead (Pb)-free and RoHS Compliant

**APPLICATIONS**

Drive laser diodes with low noise, including DPSSL, EDFA, fiber laser, direct diode lasers, etc.

**DESCRIPTION**

This laser driver is an electronic module designed for driving diode lasers with up to 2A constant current, high efficiency, low noise, high reliability, zero EMI, and small package. Figure 1 shows physical photo of CWD-01-V2-D.

It provides these functions: laser constant current control, laser current monitoring, over current and thermal protection, laser current control indication, laser diode status indication, soft start, and shut down.

It comes with a high stability low noise 2.5V voltage reference output which can be used for setting the output current. The reference output can also be used for the ADCs (Analog to Digital Converters) and/or DACs (Digital to Analog Converters).

Table 1. Pin Function Descriptions

Pin #	Name	Type	Description
1	SDN	Digital Input	Shut down control. Negative logic, at the internal chip control input: $>1.4V$ = enable, $<0.95V$ = shut down, normal threshold voltage = 1.2V.
2	LDGD	Digital Output	Laser diode good. When this pin is high, $>2V$ , the control loop is working properly. When this pin is low, $<0.3V$ , the laser diode is bad, or there is a short or open circuit at the laser diode.
3, 7	GND	Signal Ground	Signal ground pin. Connect ADCs and DACs grounds to here.
4	2.5VR	Analog Output	Reference voltage. It can source 3mA max, with 5 $\mu$ Vp-p noise @ 0.1 to 10 Hz and 25ppm/°C stability max.
5	LIS	Analog Input	Laser current set-point voltage. There is an input resistor of 100K tied to GND. Setting it from 0V to 2.5V will set the laser current from 0A to 2A linearly.
6	LIO	Analog Output	Laser current output indication. 0V to 2.5V indicates the laser current of from 0A to 2A linearly.
8	LIGD	Analog Output	Laser current good, control loop indication. When this pin is stabilized and the value is between 0.2V and 1.8V, the output voltage to the laser, Pin 9



Figure 1. Physical photo of CWD-01-V2-D

The CWD-01-V2-D is packaged in a 6 sided metal enclosure, which blocks EMIs (Electro-Magnetic Interferences) to prevent the controller and other electronics from interfering each other.

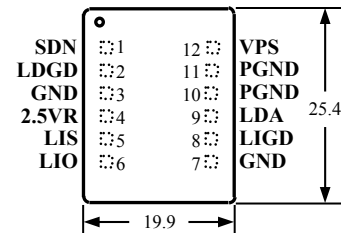


Figure 2. Pin Names and Locations

Figure 2 is the actual size top view of the CWD-01-V2-D, which shows the pin names and locations. Its thickness is 5.0mm.

**Warning:** The through hole modules can only be soldered manually on the board by a solder iron of  $< 310^{\circ}C$  ( $590^{\circ}F$ ), not go through a reflow oven process. The CWD-01-V2-D pin functions are shown in Table 1.

			LDA, will be 4.8V to 0V linearly, the laser current is stabilized, and the control loop is stable. This pin has a similar function as Pin 2 LDGD, except that this pin is of an analog output and Pin 2 is of a digital output.
9	LDA	Analog Output	Laser diode anode. Connect it to the anode of the laser diode.
10	PGND	Power Ground	Power ground pin. Connect this pin directly to the cathode of the laser diode.
11	PGND	Power Ground	Power ground pin. Connect this pin directly to power supply return pass.
12	VPS	Power Input	Power supply voltage. The driver will work from VPS = 3.3V to 5.5V.

### SPECIFICATIONS

Table 2 Characteristics ( $T_{\text{ambient}} = 25^{\circ}\text{C}$ )

Parameter	Value	Unit
Laser driver efficiency	$\geq 90$	%
Maximum output current	2	A
Current output noise	0.05	%
Stability	100	ppm/ $^{\circ}\text{C}$
Laser current control signal level	0 ~ 2.5	V
Control accuracy	$\pm 0.2$	%
Laser current indication signal level	0 ~ 2.5	V
Indication accuracy	$\pm 0.2$	%
Output reference voltage	2.5	V
Power supply voltage range	3.3 ~ 5.5	V
Operating case temperature	-20 ~ 85	$^{\circ}\text{C}$
Rise and fall times	15	$\mu\text{S}$

### OPERATION PRINCIPLE

The block diagram of the controller is shown in Figure 3. The shutdown control circuit accepts signals from 3 sources:

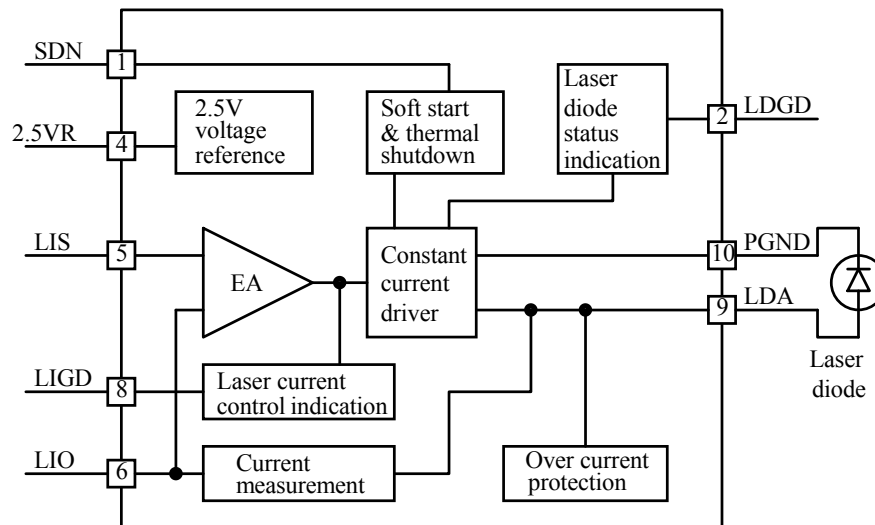


Figure 3. Block Diagram

external shut down, over current and over temperature signals. When one of these signals is activated, the controller is shut down. Only when all these 3 signals go up, the soft start circuit starts enabling the low noise driver.

It comes with a laser diode status indication circuit. When LDGD pin is high,  $>2\text{V}$ , the control loop is working properly. When this pin is low,  $<0.3\text{V}$ , the laser diode is bad, or there is a short or open circuit at the laser diode.

It comes also with a laser current control indication circuit. When this pin is stabilized and the value is between 0.2V and 1.8V, the output voltage to the laser, Pin 9 LDA, will be 4.8V to 0V linearly, the laser current is stabilized, and the control loop is stable. This pin has a similar function as Pin 2 LDGD, except that this pin is of an analog output and Pin 2 is of a digital output.

The high stability low noise 2.5V voltage reference can be used for setting the output current, and also be used for the ADCs and/or DACs.

An internal soft start circuit allows a slow start up and a quick shut down.

The current measurement circuit monitors the output current and shuts down the controller upon detecting the output current exceeds the pre-set value.

### APPLICATIONS

Figure 4 shows a typical stand-alone application circuit.

Figure 5 shows a typical micro-processor-based application circuit.

In Figure 4, the switch S1 is external shut down switch, it can turn on and off the controller with the SDN pin high and lower respectively, at the internal chip control input:  $>1.4V = \text{enable}$ ,  $<0.95V = \text{shut down}$ , normal threshold voltage =  $1.2V$ . The switch S1 can also be an electronic switch, such as an I/O pin of a micro-controller, with an either open drain or push/pull output. See Figure 5. If not using a switch (S1) to control the laser, leave the SDN pin unconnected.

In Figure 4, the LED D1 is used to indicating laser diode status. When LDGD pin is high,  $>2V$ , the laser diode control loop is working properly. When LDGD pin is low,  $<0.3V$ , the laser diode is bad, or there is a short or open circuit at the laser diode. The LDGD pin can also be connected to a digital input pin of a micro-controller, when software/firmware is utilized in the system. See Figure 5.

In Figure 4, the adjustable resistor W1 is used to setting the output current. Setting LIS from  $0V$  to  $2.5V$  will set the laser current from  $0A$  to  $2A$  linearly.

The laser diode D2 is connected between LDA and PGND. It is worth mentioning that the power supply return terminal should be connected to the pin 11 PGND and the cathode of the laser diode should be connected to the pin 10 PGND. These 2 nodes should not be connected together externally and they are connected together internally already by the controller.

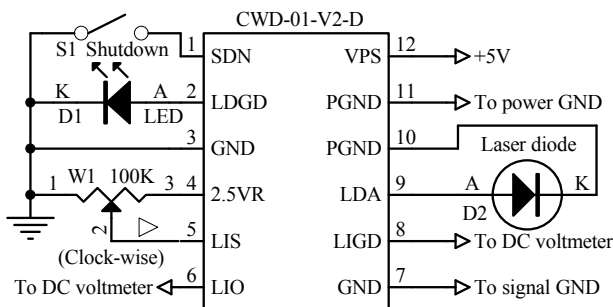


Figure 4. A Typical Stand-alone Application Schematic

### Turning the Controller On and Off

The controller can be turned on and off by setting the SDN pin high and lower respectively. It is recommended to turn the controller on by this sequence:

To turn on: turn on the power by providing the power supply voltage to the controller, turn on the controller by releasing the SDN pin.

To turn off: turn off the controller by lowering the voltage

of SDN pin, turn off the power by stopping the voltage supply on the VPS pin.

When not controlling by the SDN pin: leave it unconnected and turn on and off the controller by the power supply.

### Adjusting the Output Current

The output current is set by adjusting W1, which sets input voltages of LIS, pin 5. See Figure 4. The output current will be:

$$I_{OUT} = 2.0 \times V_{LIS} / 2.5 \text{ (A)}$$

LIS can be configured by using a DAC, to replace the W1 in Figure 4. Make sure that the DAC has output low noise, or, if no modulation is needed, an RC low pass filtered by be inserted between the DAC and the LIS pin. See Figure 5.

The LIO can still be used to monitor the output current when the LIS is adjusted.  $0V$  to  $2.5V$  indicates the laser current of from  $0A$  to  $2A$  linearly.

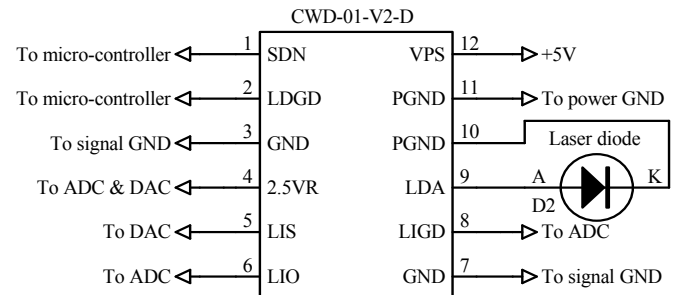


Figure 5. A Typical Micro-processor-based Application

### Monitoring the Output Current

The output current of the controller can be monitored by measuring the voltage on the LIO pin. This feature is very useful for micro-controller based system where the ADC is available and monitoring the current in real time is required. This pin provides a very low noise voltage signal which is proportional to the output current:

$$LIO \text{ (V)} = 2.5 \times I_{OUT} / 2.0 \text{ (V)}$$

For example, when the output signal equals to  $2.5V$ , the output current is  $2A$ .

LIO can be used to drive an ADC directly, and also be measured by a multimeter during debugging process.

### Controller Power Consumption

The power consumption of the controller can be calculated by:

$$P_{\text{controller}} = I_{IN} \times V_{PS} - I_{OUT} \times V_{LDA}$$

where  $I_{OUT}$  is the output current;

$I_{IN}$  is the power supply input current;

$V_{PS}$  is the power supply voltage;

$V_{LDA}$  is the voltage across the laser diode;

$$\eta = I_{OUT} \times V_{LDA} / (I_{IN} \times V_{PS})$$

When the P\_controller exceeds 1W, a heat sink might be needed. The best way for arranging the heat sinking for the driver is as follows: transferring the heat by sandwiching a piece of thermal conductive pad between the top metal surface of the laser driver and the internal metal surface of the final product as shown in Figure 6.1 and 6.2 below. The recommended thickness of the thermal conductive pad in Figure 6.1 is 1~4mm, and in Figure 6.2 is 0.5mm. ATI also provides a series of thermal conductive pads, click [here](#) for more information.

If prefer not to use the heat sink, this is an option: lowering the controller power consumption by reducing the power supply voltage  $V_{PS}$ . Please make sure:

$$V_{PS} \geq V_{laser\_diode\_max} + 0.15 V_{PS}$$

where  $V_{laser\_diode\_max}$  is the maximum possible laser diode voltage.

### First Time Power Up

Laser is a high value and vulnerable device. Faults in connections and damages done to the controller during soldering process may damage the laser permanently.

**To protect the laser, it is highly recommend to use 3 to 4 regular diodes of >2A to form a “dummy laser” and insert it in the place of the real laser diode, when powering up the controller for the first time. Use an oscilloscope to monitor the LDA voltage at times of power-up and power-down, make sure that there is not over-shoot in voltage. At the same time, use an ammeter in serious with the dummy laser, to make sure that the output current is correct.**

After thorough checking free of faults, disconnect the dummy laser and connect the real laser in place.

The controller output voltage range for the laser is between 0.5 to 4V when powered by a 5V power supply.

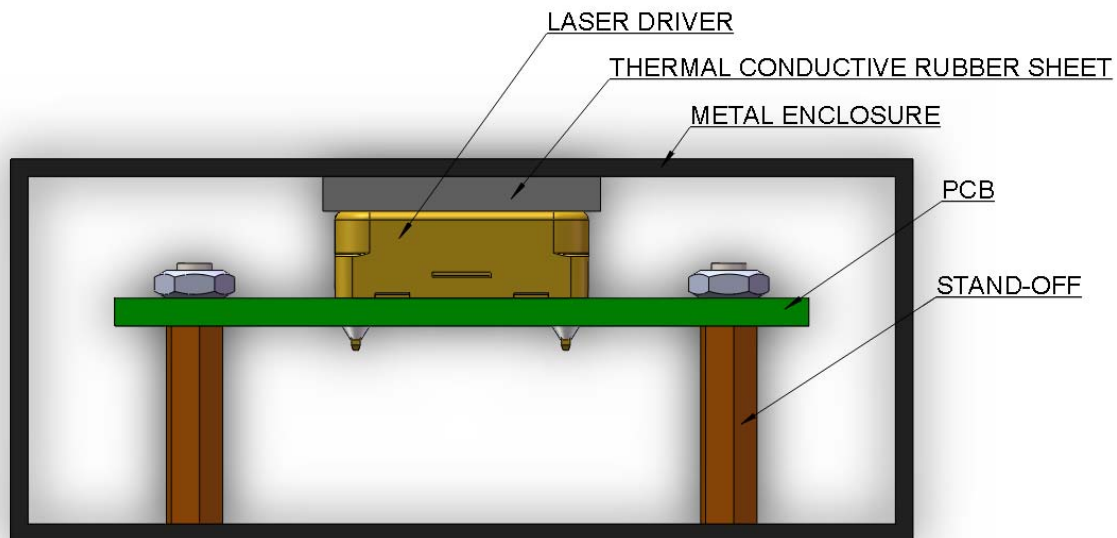


Figure 6.1 Transferring Heat with Metal Enclosure

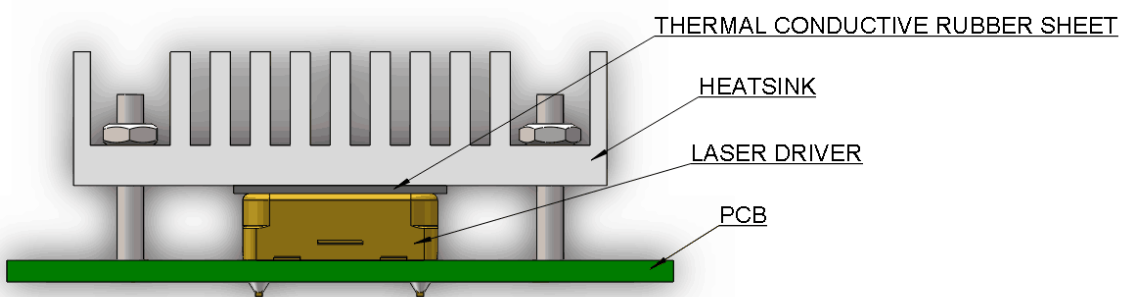


Figure 6.2 Transferring Heat with Heat Sink

**OUTLINE DIMENSIONS AND MOUNTING**

The controller comes in only one package: through hole mount. It is often called DIP (Dual Inline package) or D (short for DIP) package and has a part number: CWD-01-V2-D. Dimensions of the DIP package controller are shown in Figure 7.

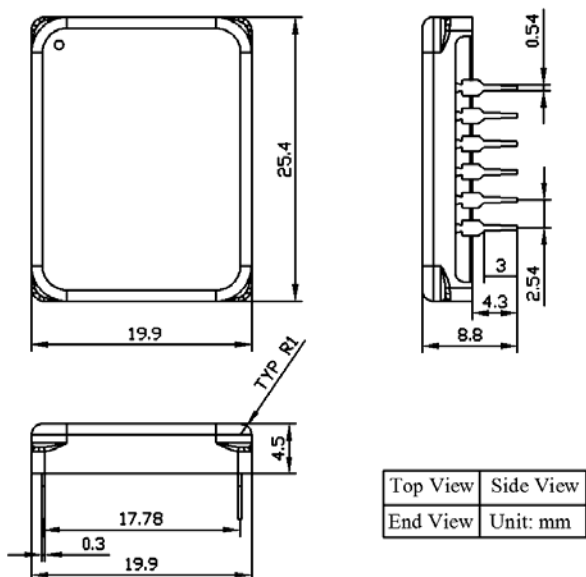


Figure 7. Dimensions of the DIP Package Controller

Figure 8 shows the foot-print which is seen from the end side of the PCB.

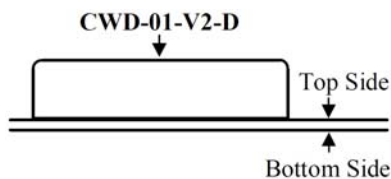


Figure 8. End View of the PCB Foot-print

Figure 9 shows the foot-print which is seen from the top side of the PCB, therefore, it is a “see through” view.

“Tent” (i.e. cover the entire via by the solder mask layer) all the vias under the controller, otherwise, the vias can be shorted by the bottom plate of the controller which is internally connected the ground.

See Figure 9 and 10, it is recommended to use large copper fills for VPS, PGND, and the LDA pins, and other pins if possible, to decrease the thermal resistance between the module and the supporting PCB, to lower the module temperature.

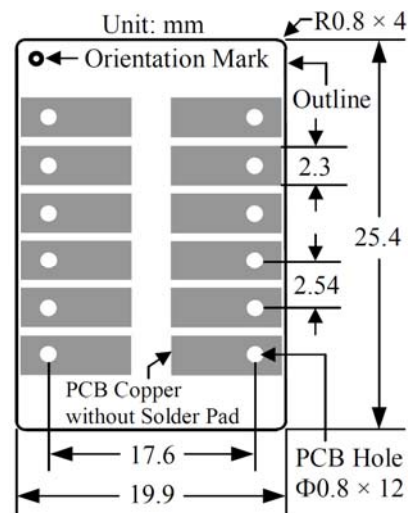


Figure 9. Top View of the Top Side PCB Foot-print

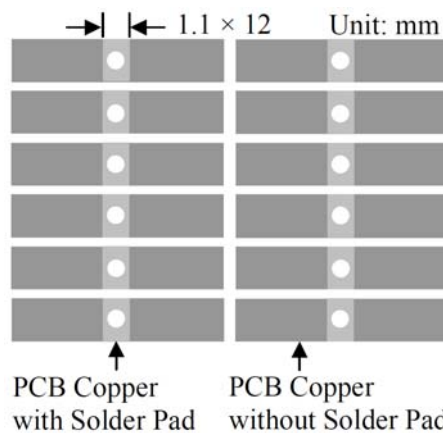


Figure 10. Top View of the Bottom Side PCB Foot-print

Figure 10 shows the foot-print which is seen from the bottom side of the PCB.



**Warning:** The through hole modules can only be soldered manually on the board by a solder iron of < 310°C (590°F), not go through a reflow oven process.

**NOTE:** The power supply may have overshoot, when happens, it may exceed the maximum allowed input voltage, 6V, of the controller and damage the controller permanently. To avoid this from happening, do the following:

1. Connect the controller solid well with the power supply before turning on the power.
2. Make sure that the power supply has sufficient output current. It is suggested that the power supply can supply 1.2 to 1.5 times the maximum current the controller requires.
3. When using a bench top power supply, set the current limit to >1.5 times higher than the maximum current the controller requires.

**ORDERING INFORMATION**

Table 3. Part Number

Part #	Description
CWD-01-V2-D	It can output up to 2A current and is packed in DIP package.

Table 4. Unit Price

Quantity	1 – 9	10 – 49	50 – 199	200-499	≥500
CWD-01-V2-D	\$76.00	\$73.00	\$70.00	\$67.00	\$64.00

**NOTICE**

1. ATI warrants performance of its products for one year to the specifications applicable at the time of sale, except for those being damaged by excessive abuse. Products found not meeting the specifications within one year from the date of sale can be exchanged free of charge.
2. ATI reserves the right to make changes to its products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete.
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